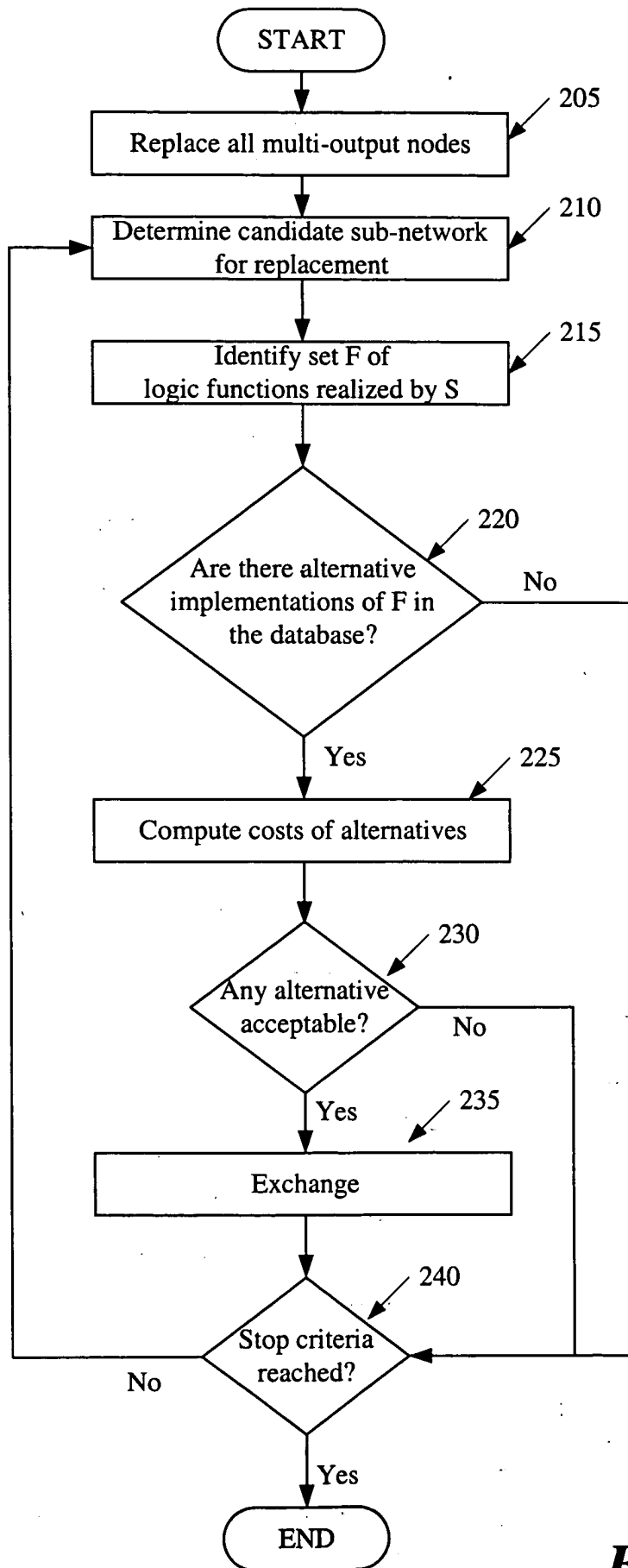


Figure 1



**Figure 2**

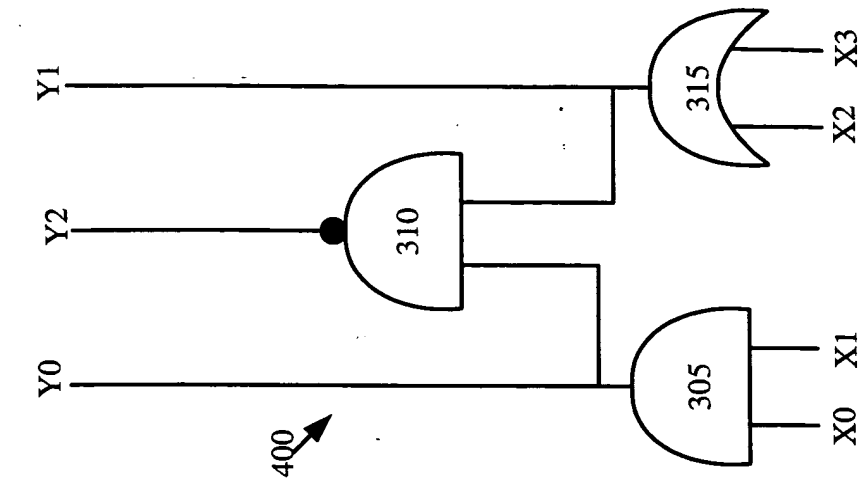


Figure 4

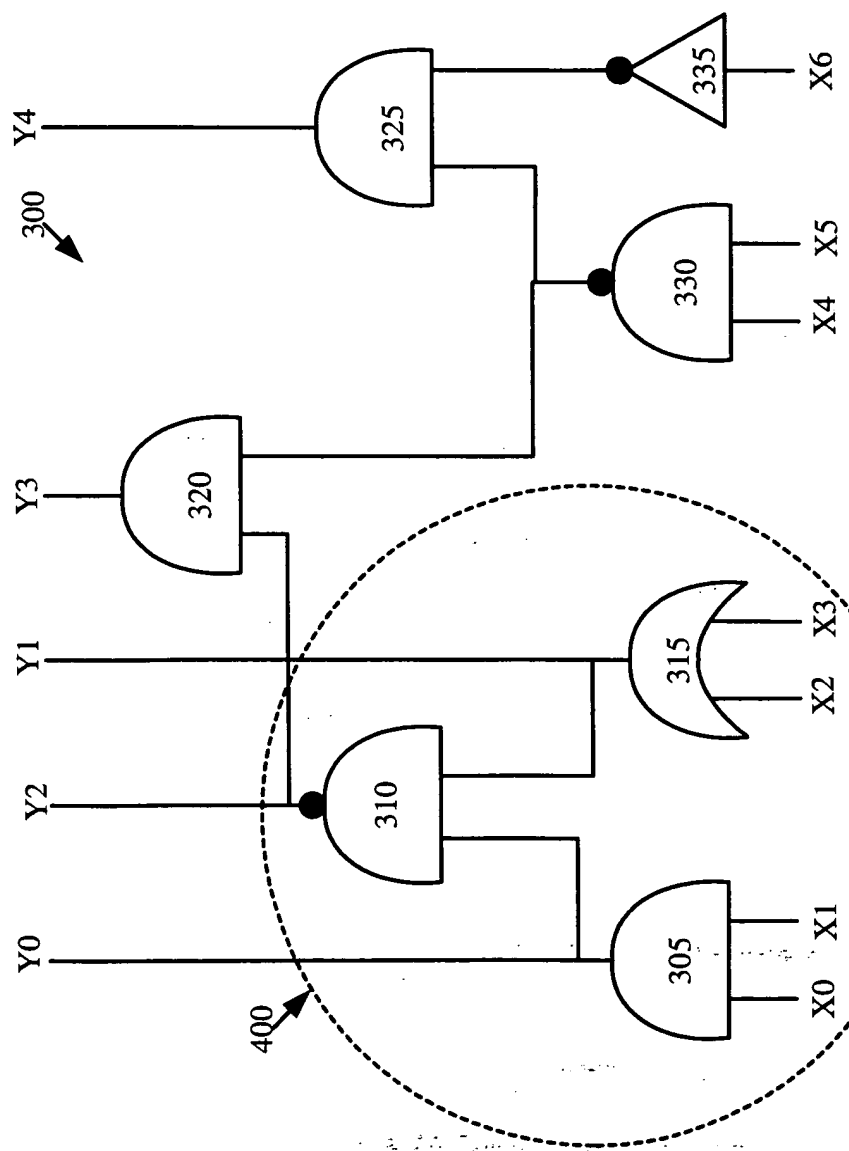
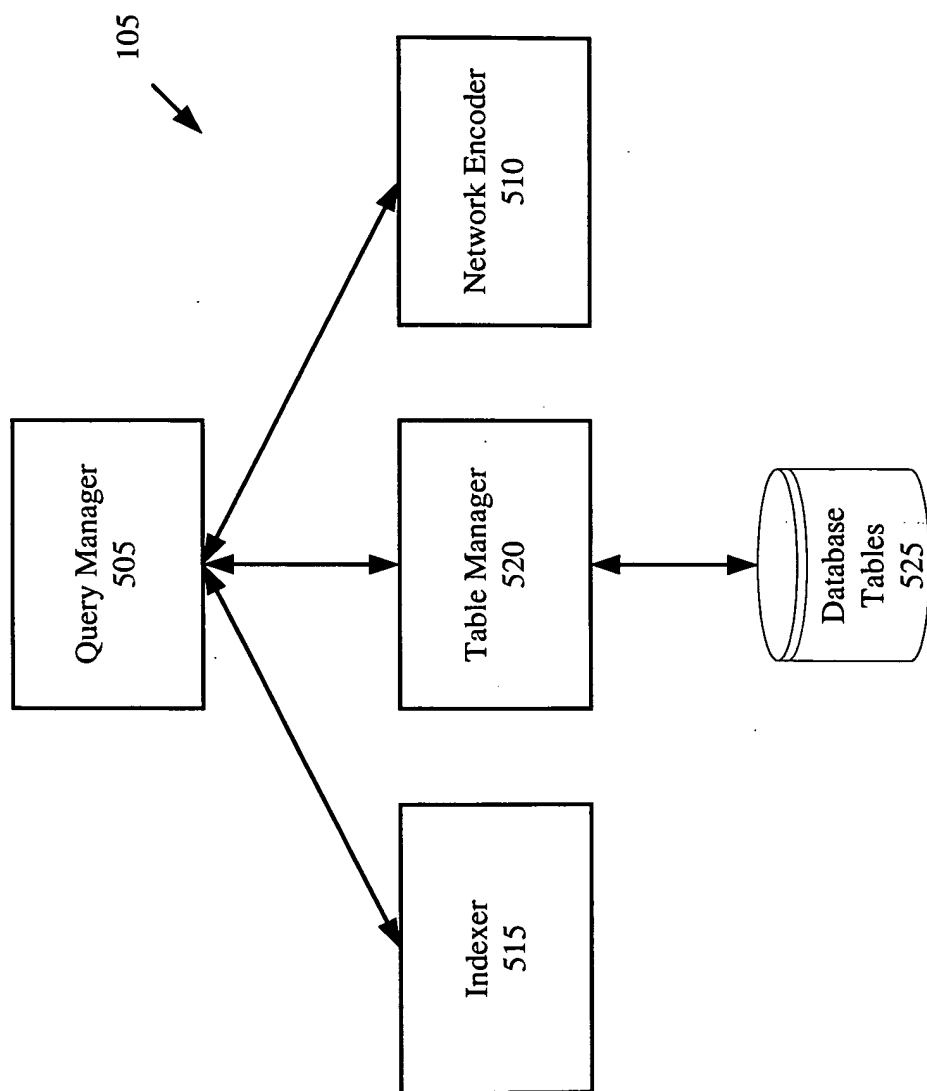
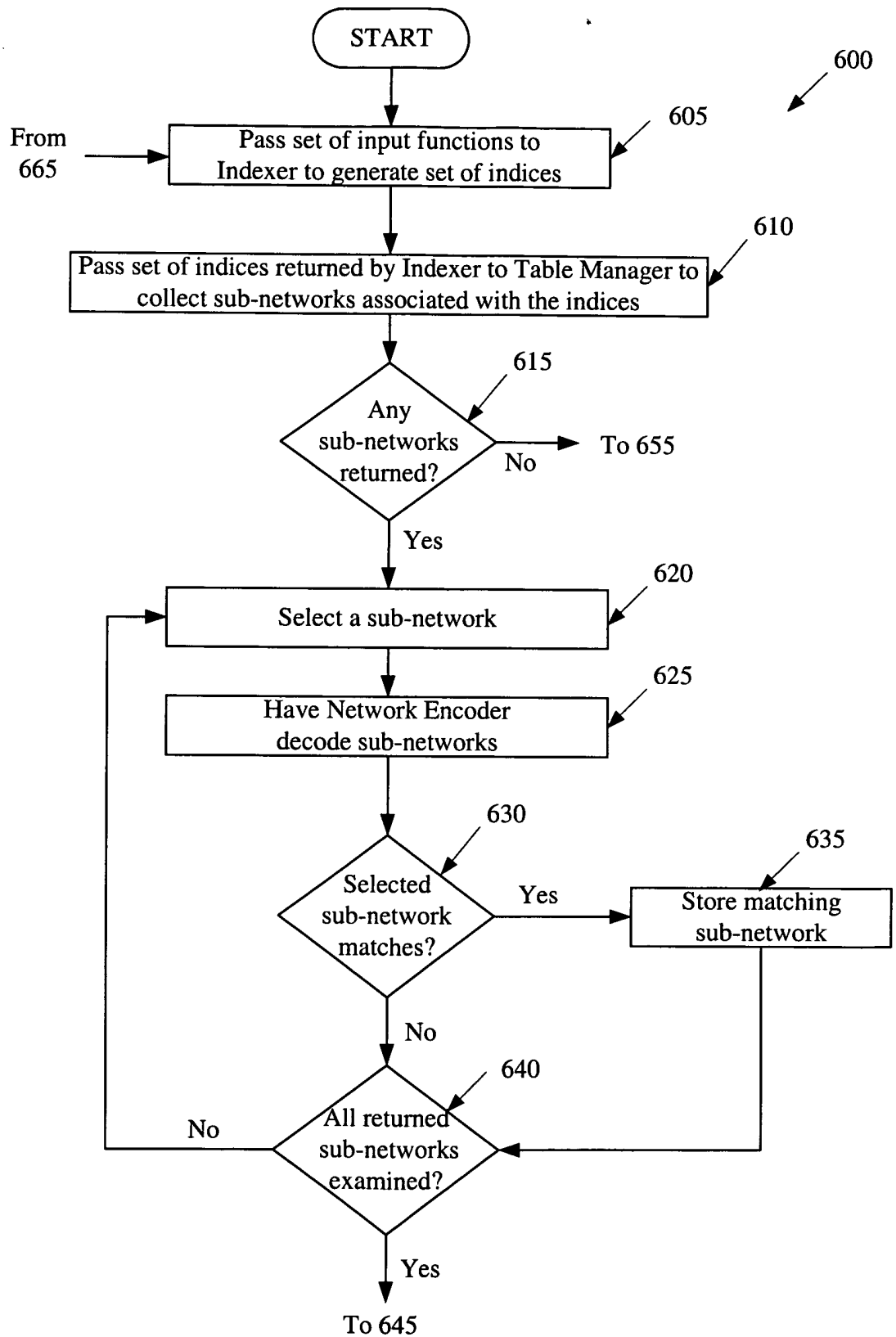


Figure 3

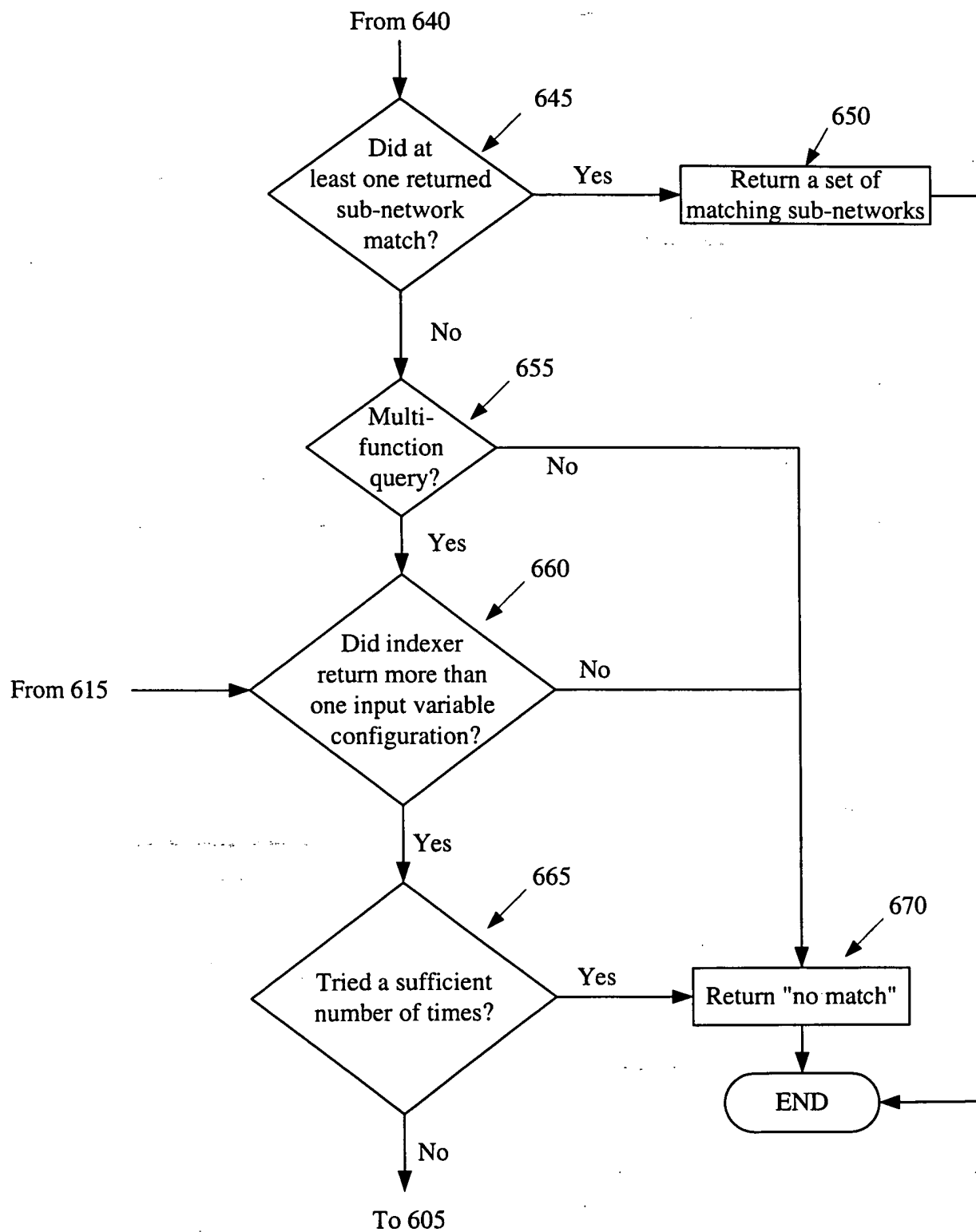


**Figure 5**

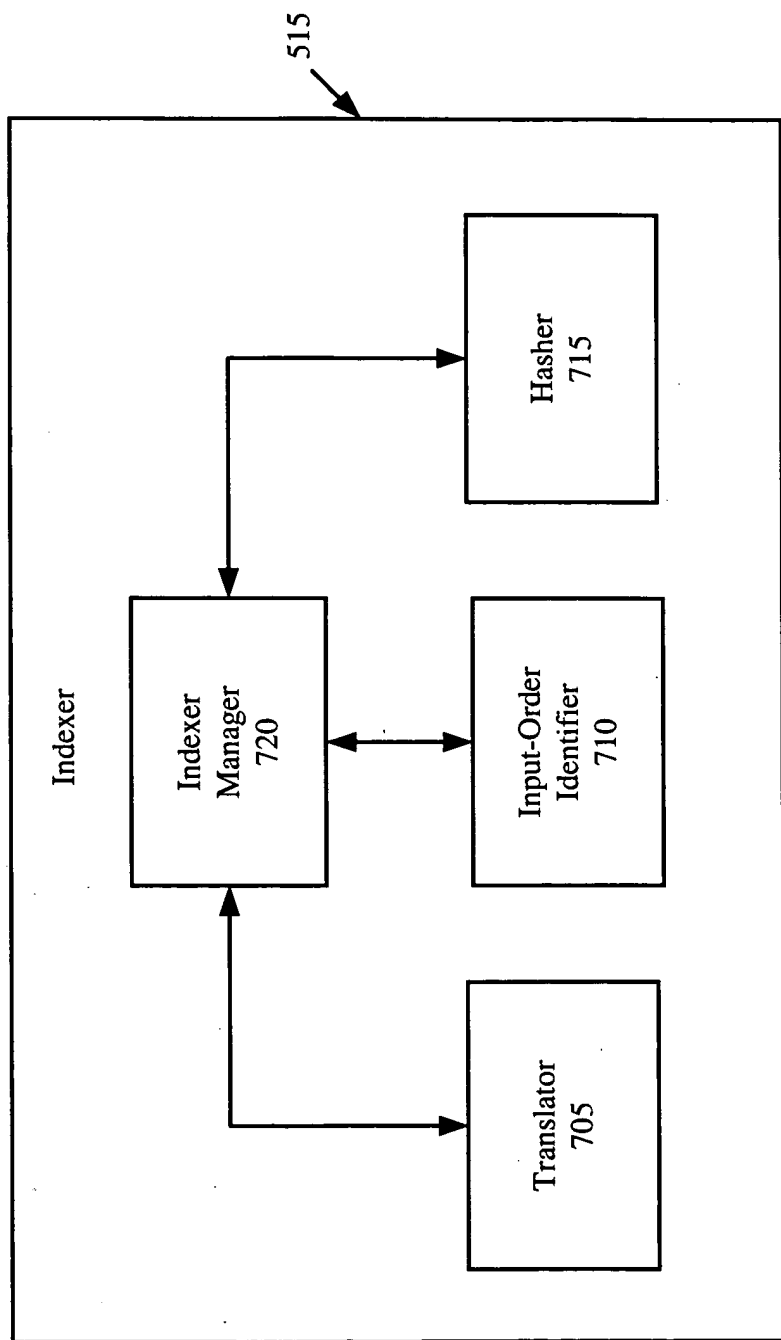


**Figure 6A**

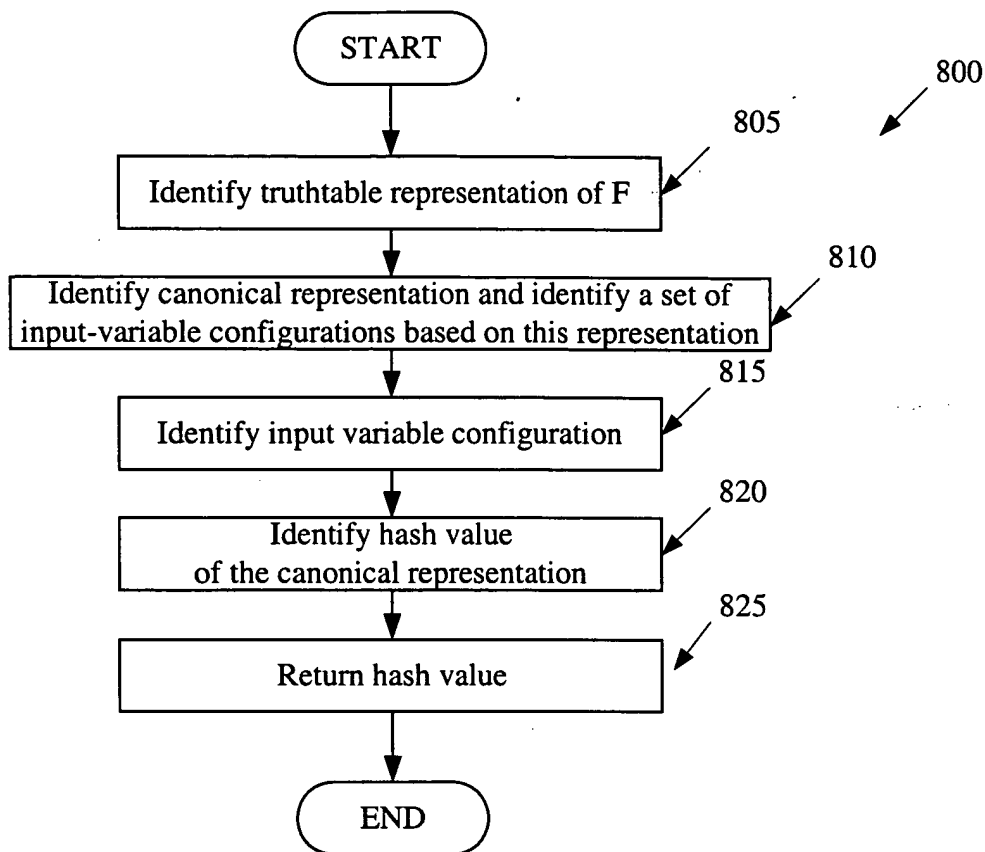
**Figure 6:** Figure 6A  
Figure 6B



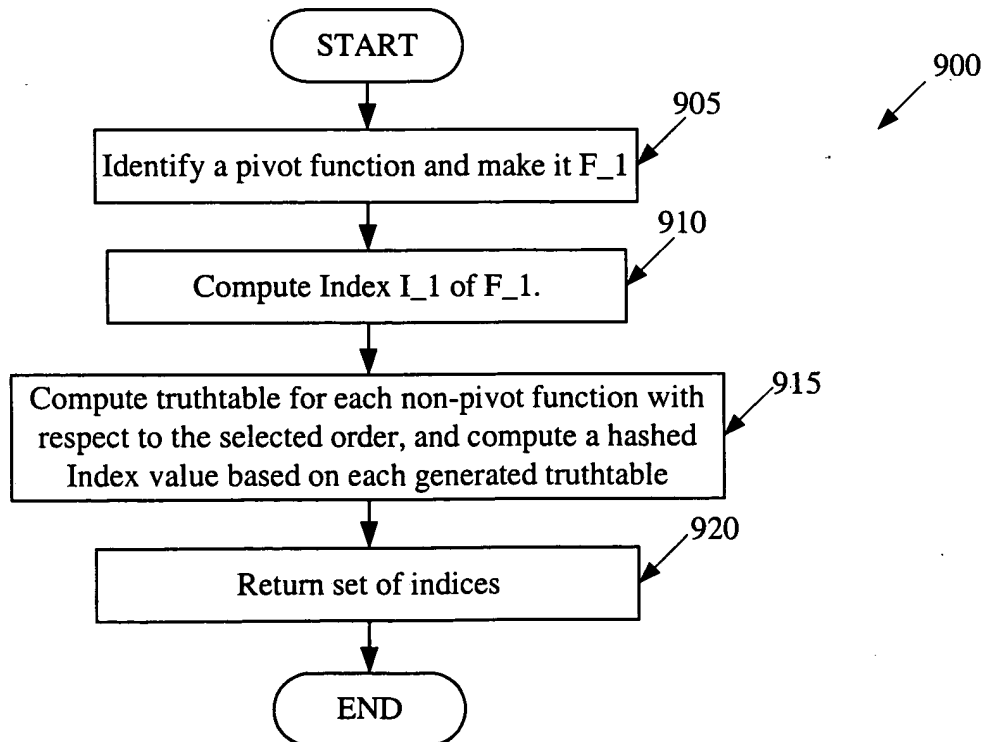
**Figure 6B**



**Figure 7**



**Figure 8**



**Figure 9**





FIG. 13 is a logic diagram of a circuit 1105, which is a logic circuit for implementing the logic function F defined by the truth table in FIG. 14.

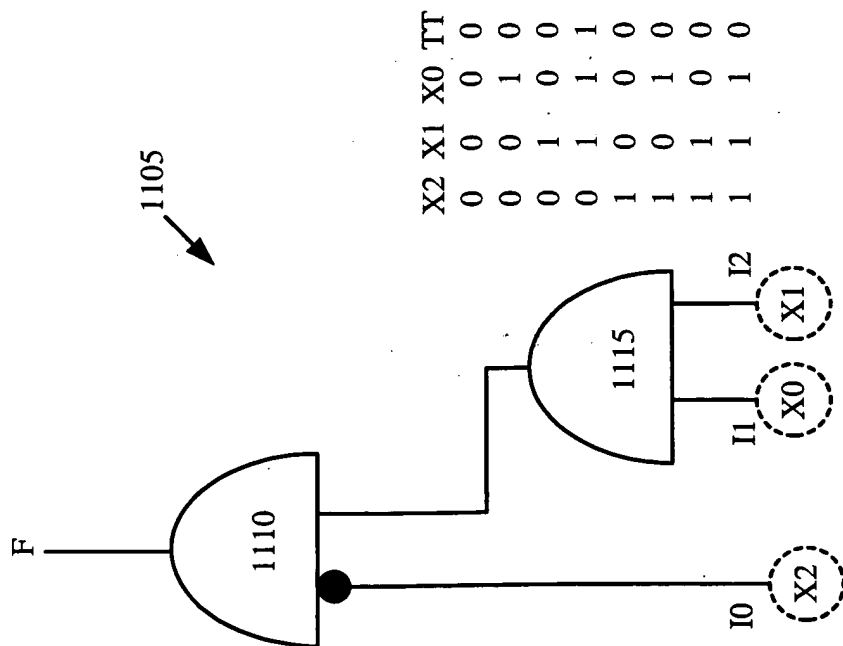


Figure 12

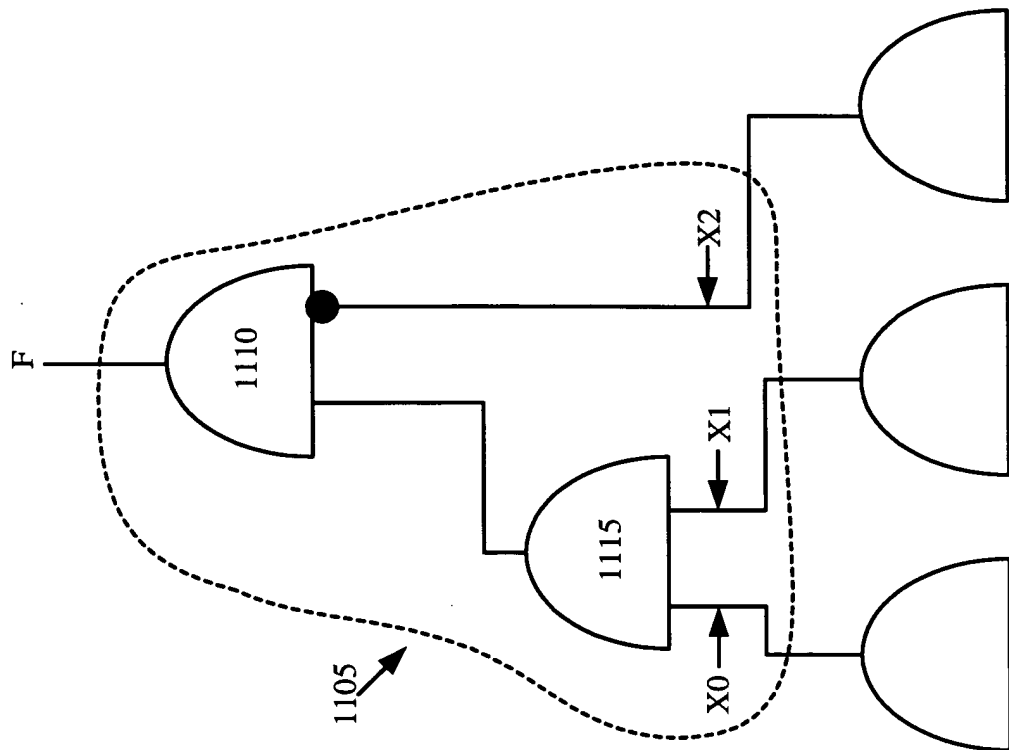
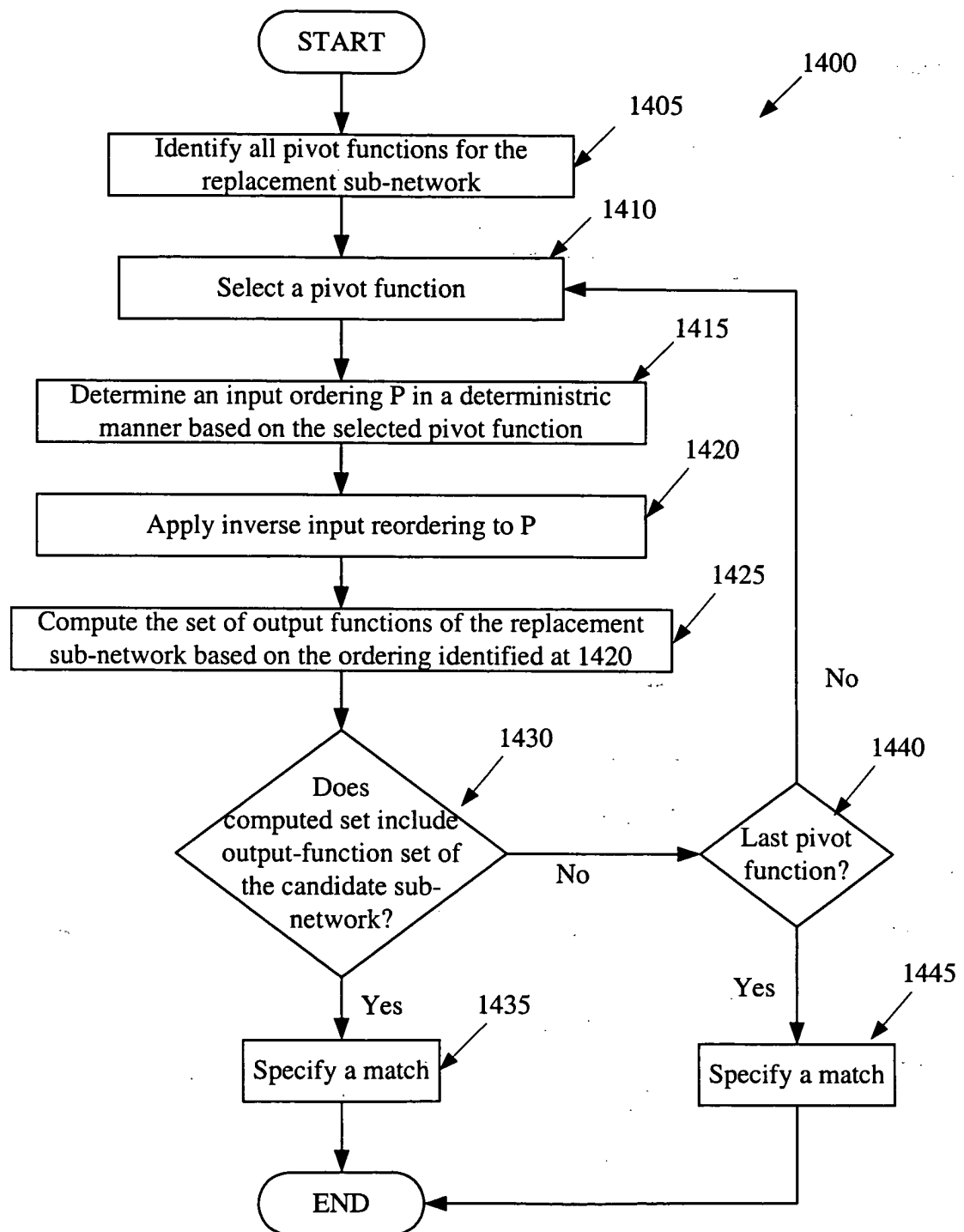


Figure 13



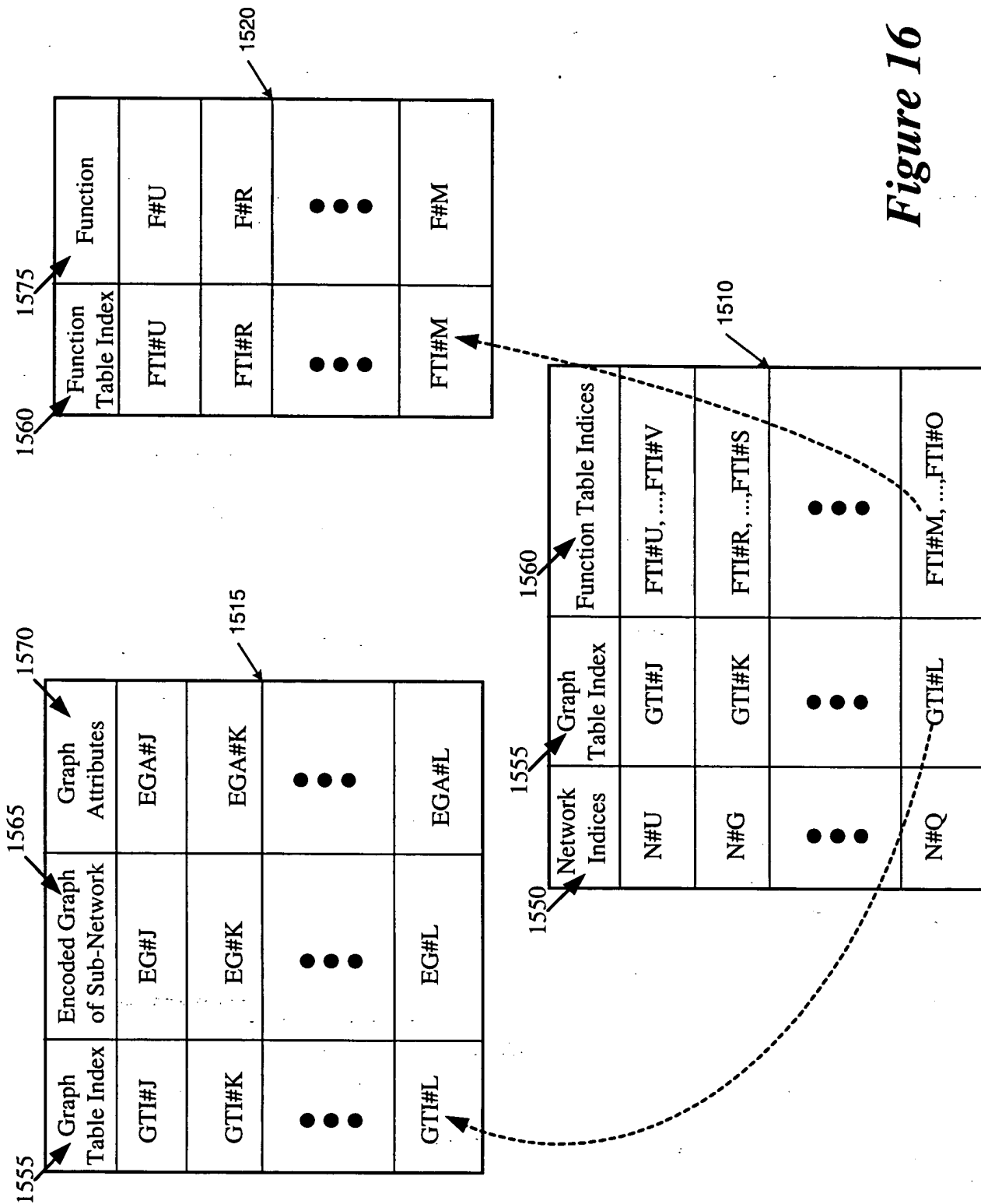
**Figure 14**

FIG. 15 is a schematic diagram of a network structure 1500. The network structure 1500 includes a set of nodes 1505 and a set of links 1510. The nodes 1505 are arranged in a grid-like pattern, and the links 1510 connect the nodes 1505. The network structure 1500 is used to illustrate a network topology.

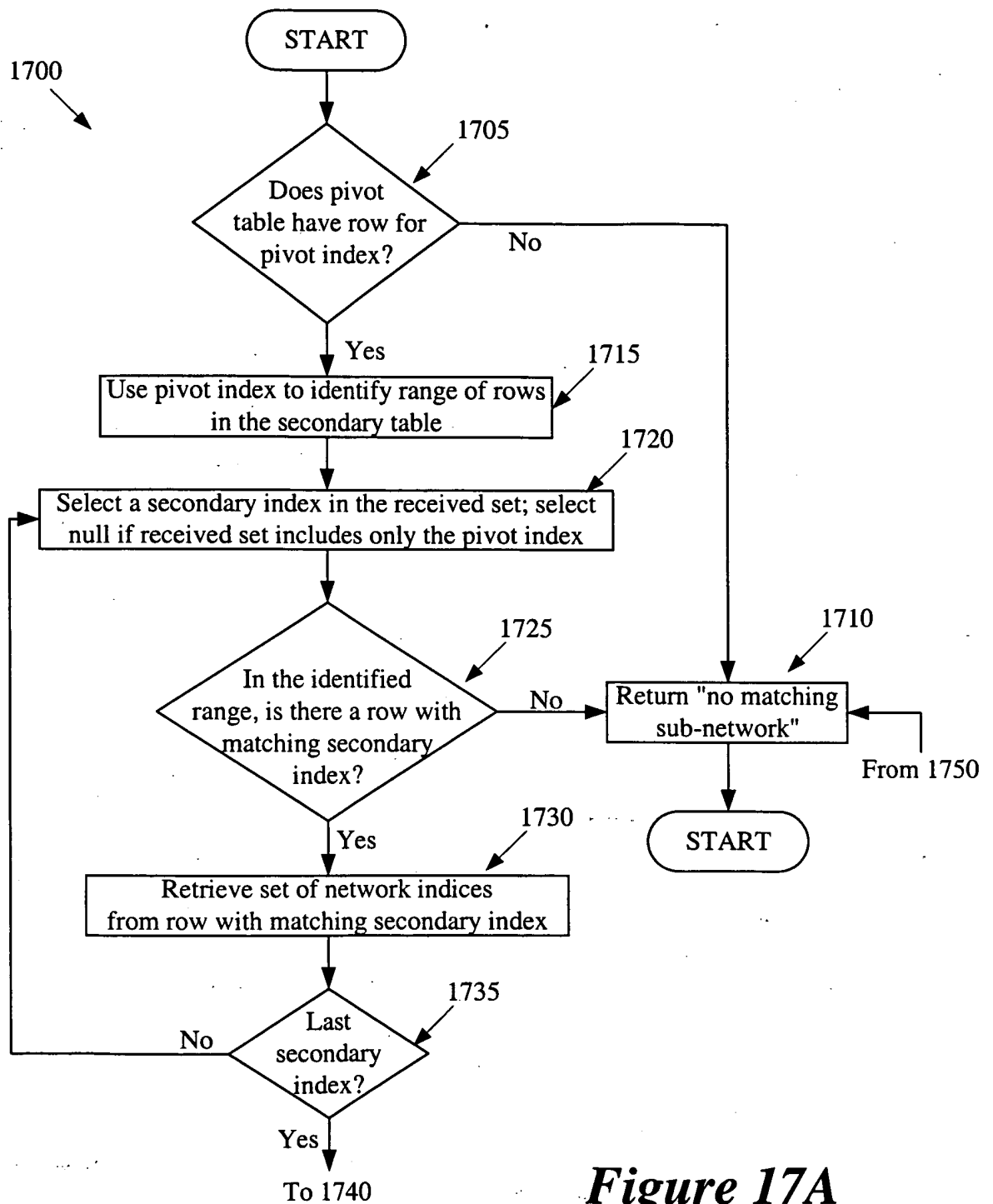
1525 Pivot Index	1545 Secondary Index	1550 Network Indices	1540 Row Length
Pivot Index #1	Index #T	N#U, ..., N#V	Length A
• • •	• • •	• • •	•
Pivot Index #1	Index #J	N#D, ..., N#E	Length B
• • •	• • •	• • •	• •
Pivot Index #X	Index #H	N#F, ..., N#G	Length C

1525 Pivot Index	1530 First Row	1535 Last Row
Pivot Index #1	Row #P	Row #Q
Pivot Index #2	Row #R	Row #S
•	• • •	• • •
Pivot Index #X	Row #Y	Row #Z

Figure 15

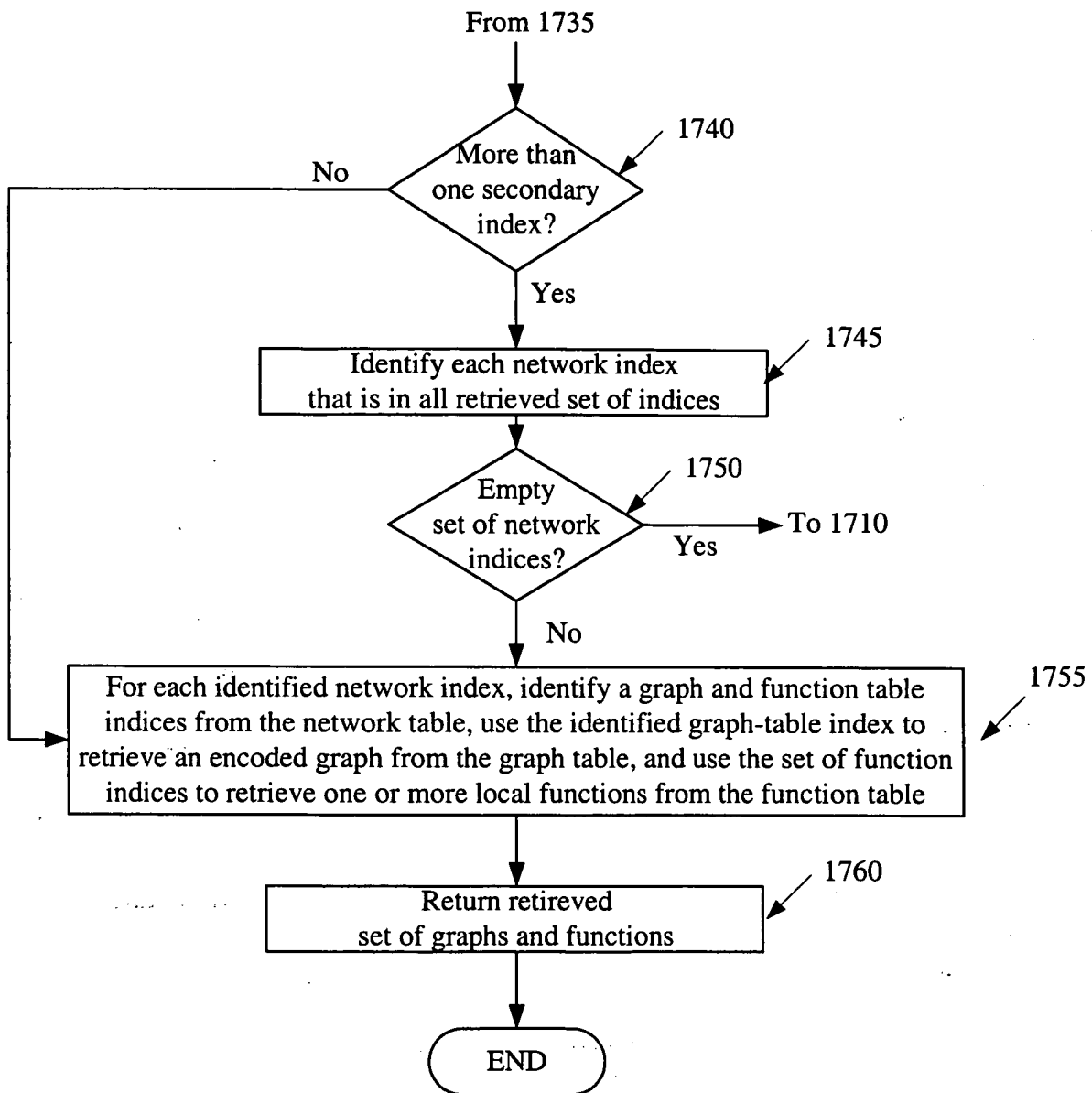


**Figure 16**

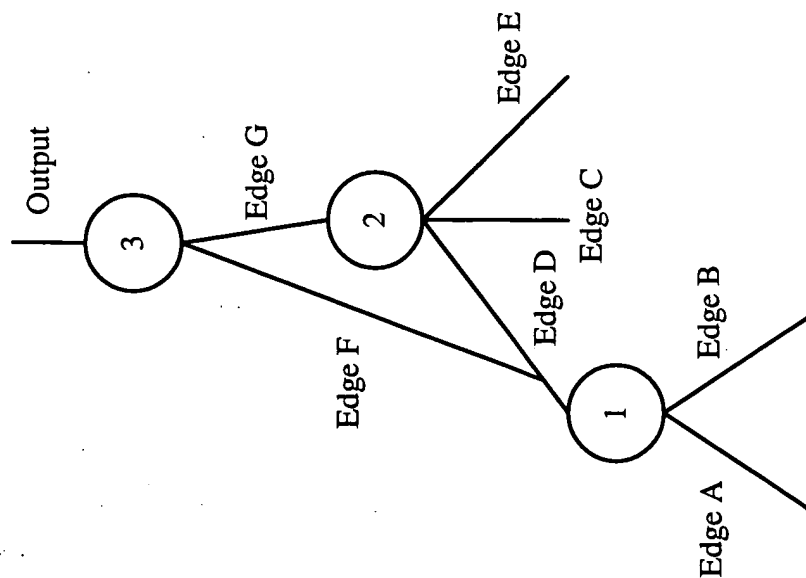


**Figure 17A**

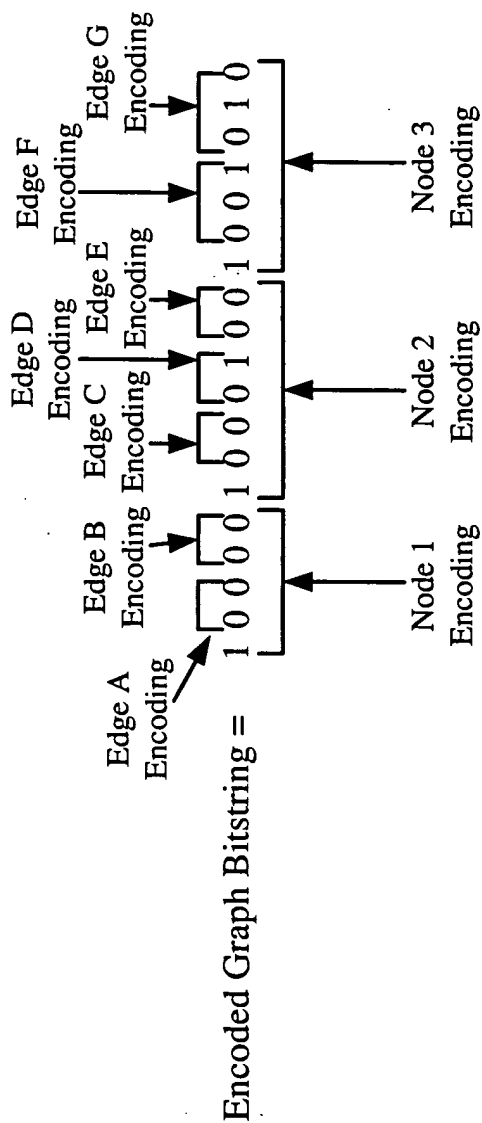
**Figure 17:** *Figure 17A*  
*Figure 17B*



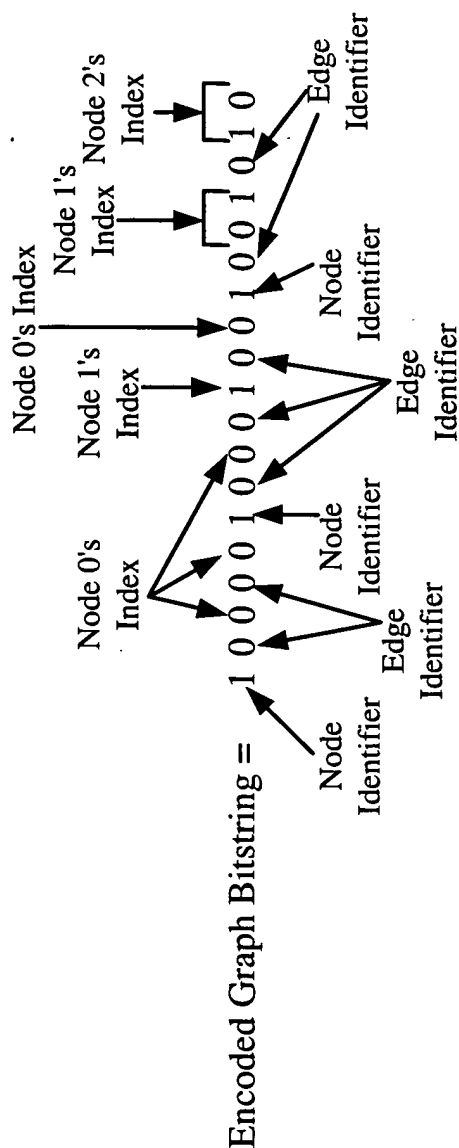
**Figure 17B**



**Figure 18**

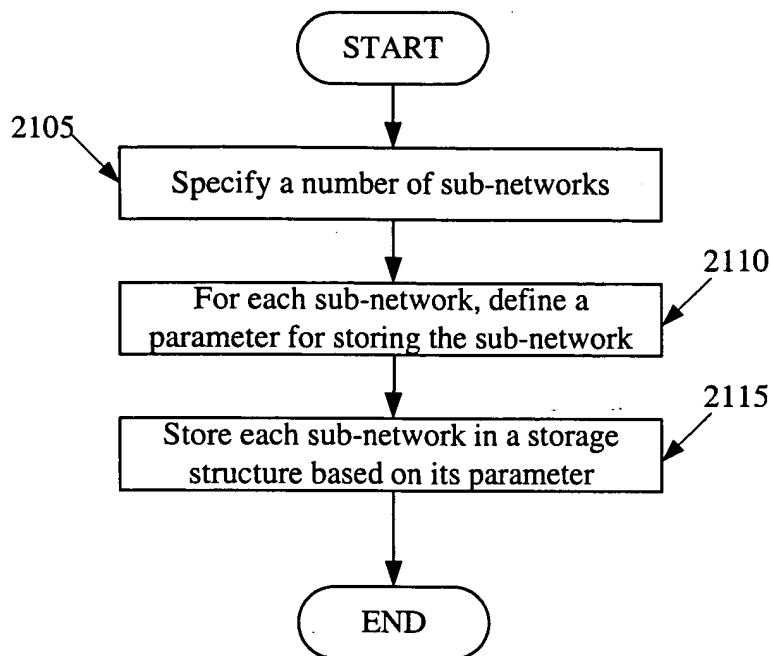


**Figure 19**

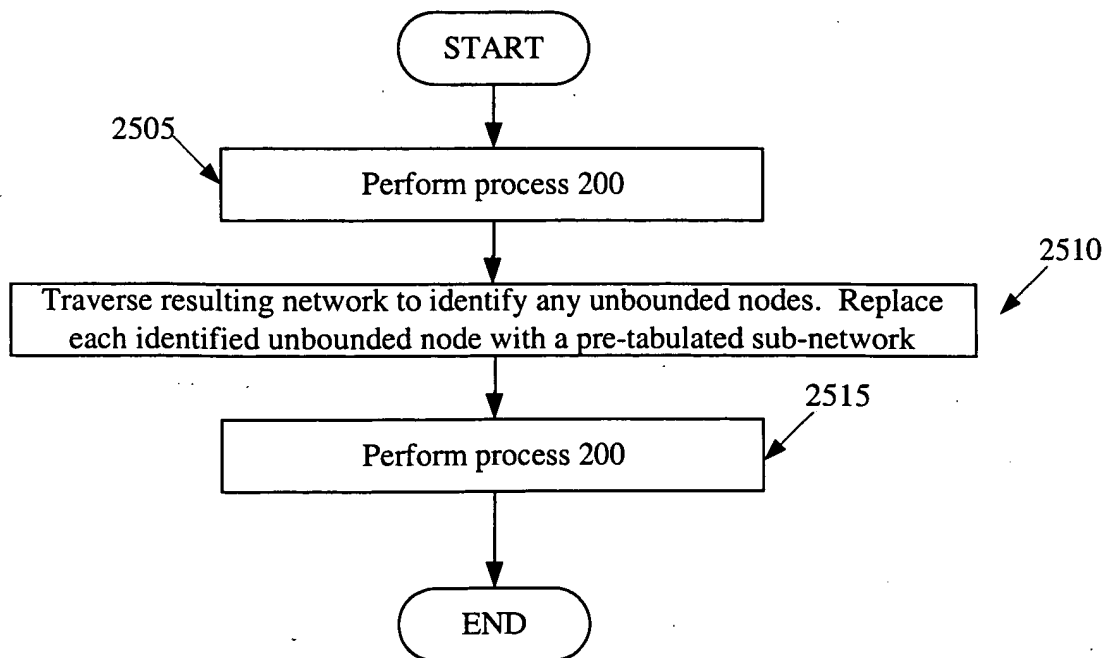


**Figure 20**





**Figure 21**



**Figure 25**

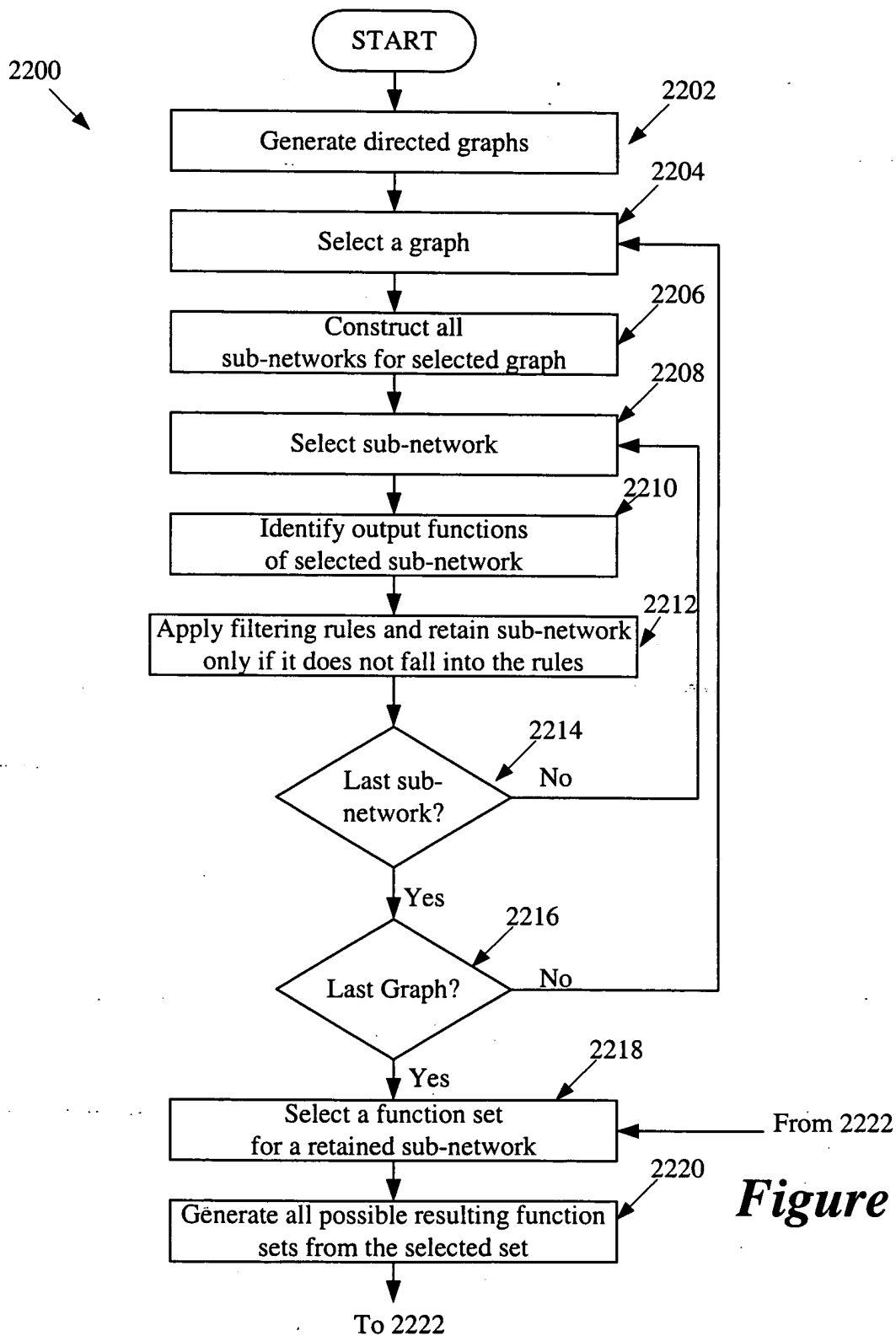
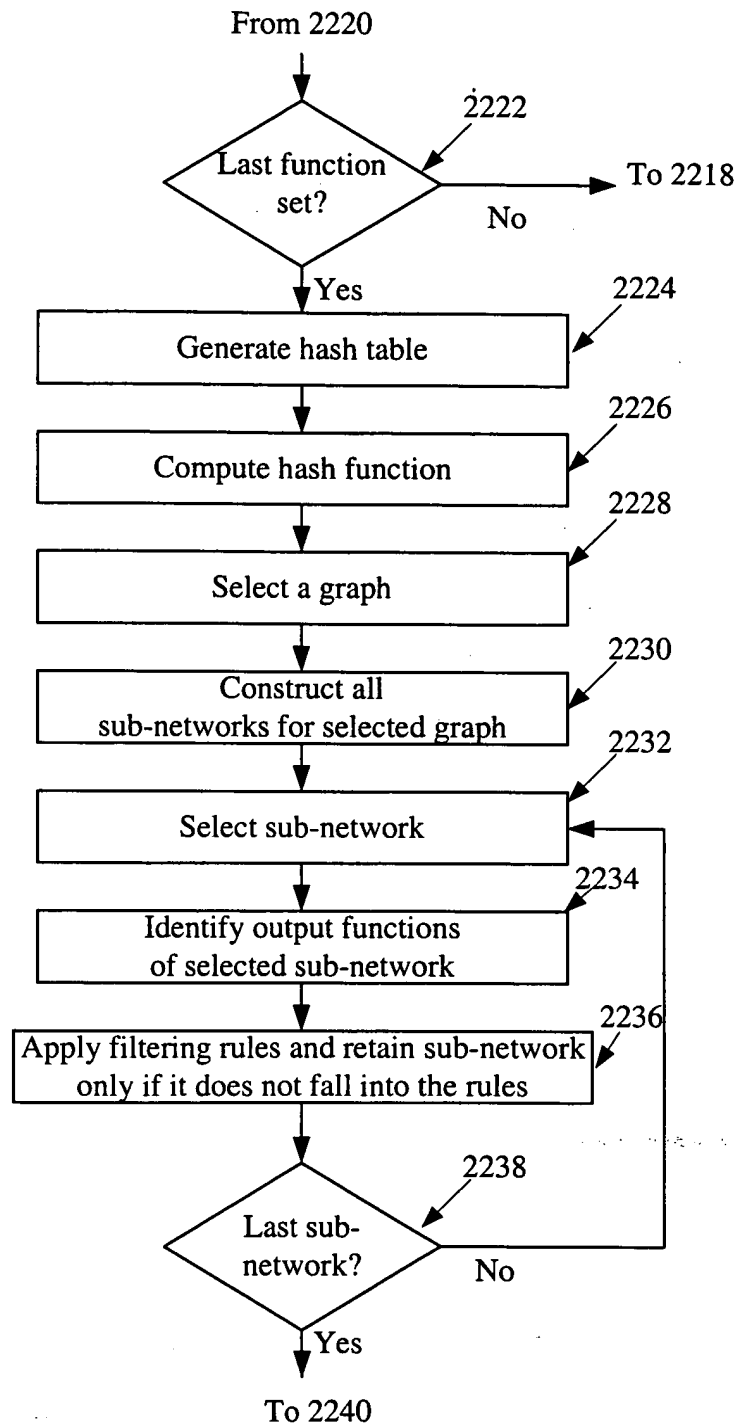
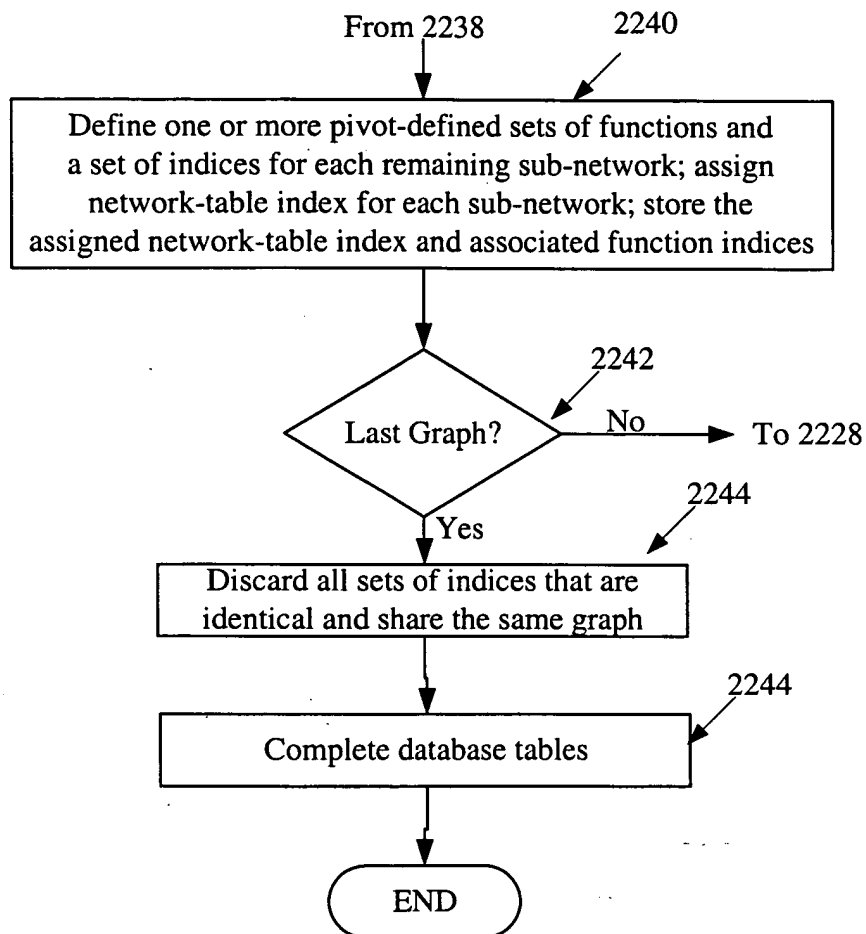


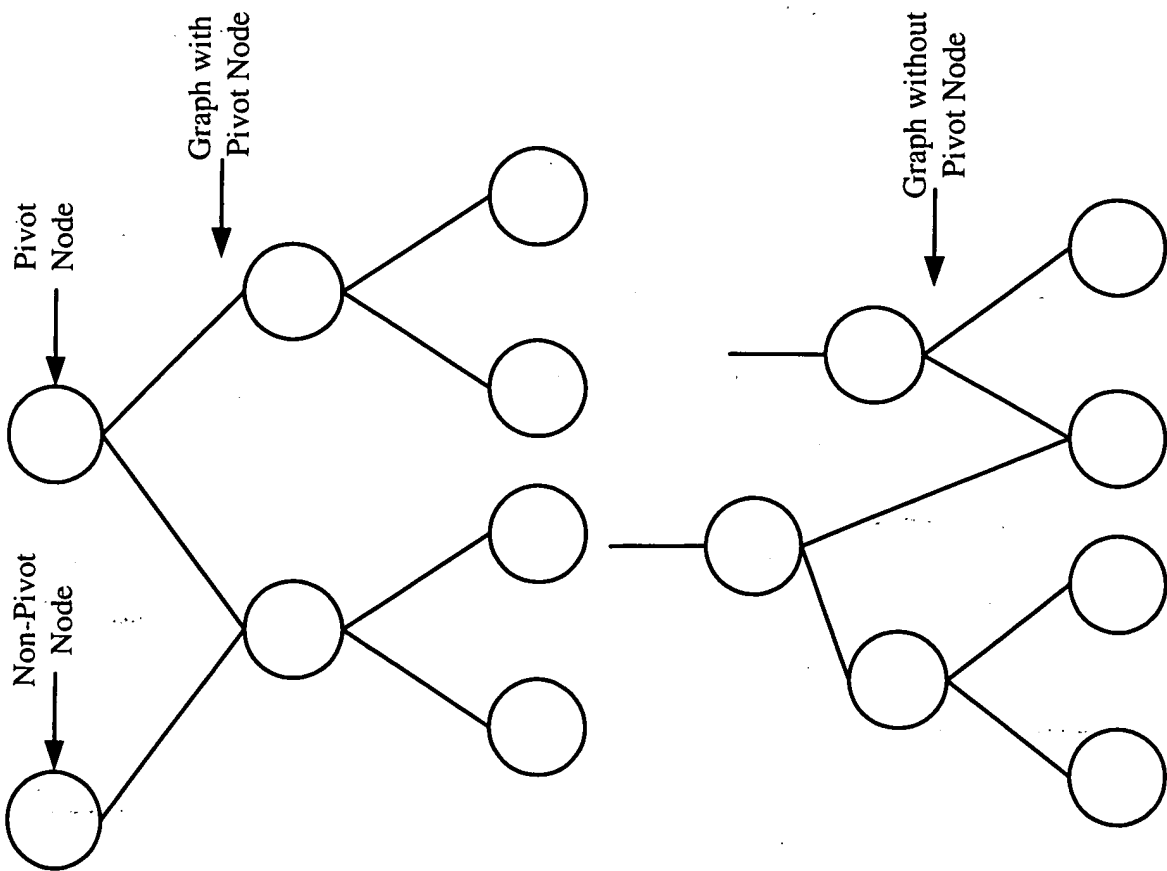
Figure 22: Figure 22A  
Figure 22B  
Figure 22C



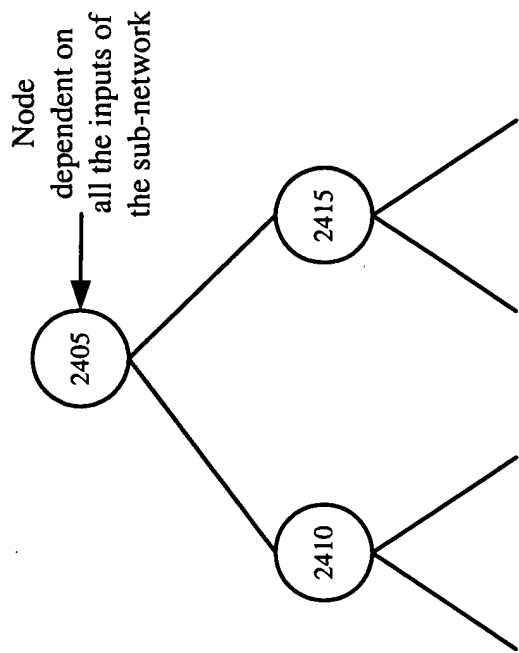
**Figure 22B**



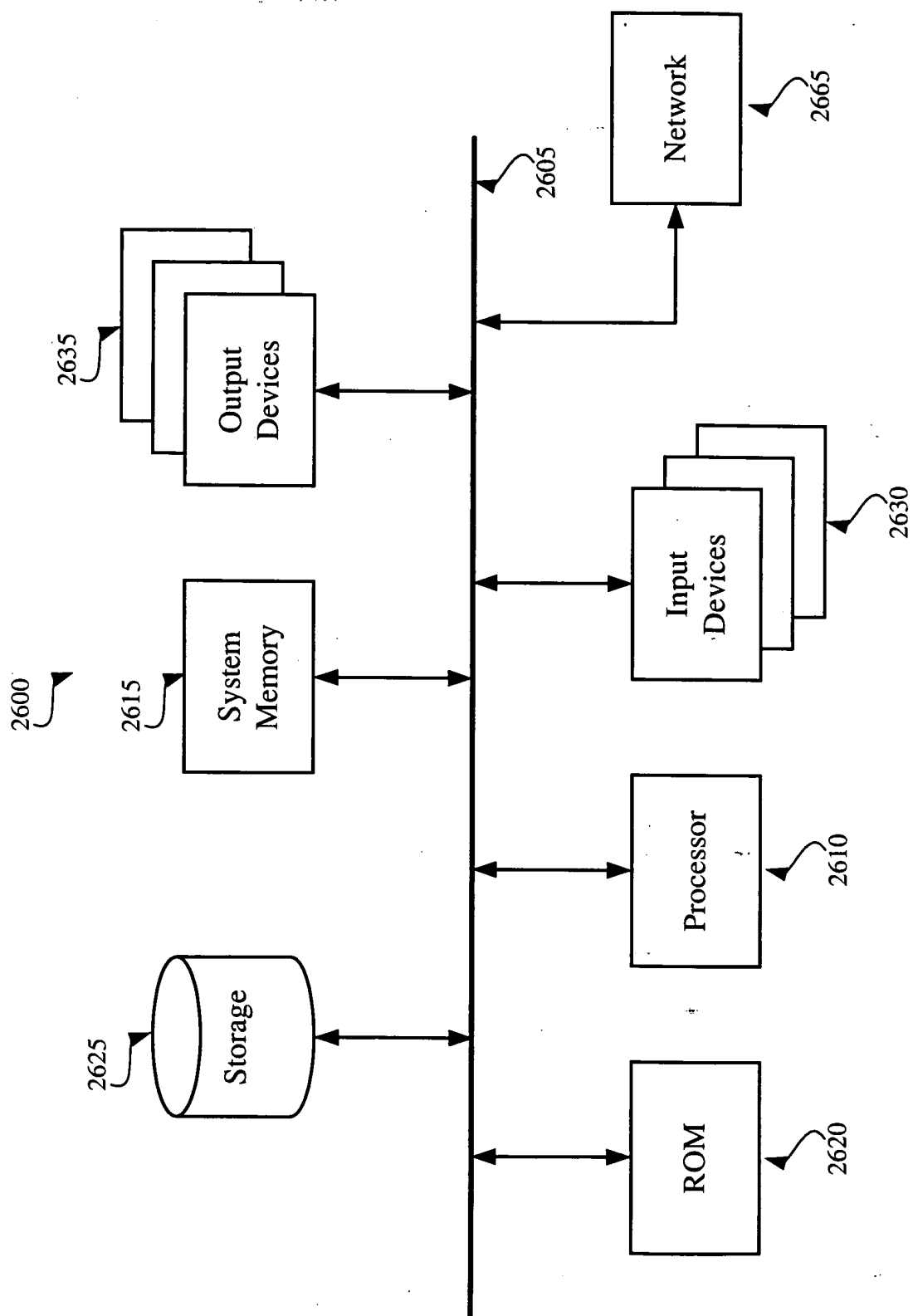
**Figure 22C**



**Figure 23**



**Figure 24**



**Figure 26**